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decoupling the data transfer between the CPU module 3
(operated at high data transmission rates), and the CAN module
(operated at relatively low data transmission rates). ~

In the Claims:

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Claim 1 (twice amended). In a data transmission system with
at least two subscribers, a memory device to be connected, for
serial data transfer of binary data objects of a predetermined
data width, between the at least two subscribers, comprising:

a multiplicity of memory objects each being identifiable by a
respective address;

each said memory object having a data width being at least as
large as a predetermined data width of a data object intended
for data transfer, a data region storing the data objects, and
a control region containing monitoring and control functions
for the data transfer;

at least one FIFO structure containing a plurality of said
memory objects and transmitting data in a data-controlled data
transfer controlled by the data objects being transmitted.

Please Add the Following Claim:

--26. In a data transmission system with at least two subscribers, a memory device to be connected, for serial data transfer of binary data objects of a predetermined data width, between the at least two subscribers, comprising:

a multiplicity of memory objects each being identifiable by a respective address;

each said memory object having a data width being at least as large as a predetermined data width of a data object intended for data transfer, an identification region containing the respective address of said memory object, a data region storing the data objects, and a control region containing monitoring and control functions for the data transfer;

at least one FIFO structure containing a plurality of said memory objects and transmitting data in a data-controlled data transfer controlled by the data objects being transmitted.--

Remarks:

Reconsideration of the application is requested.

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